RAMP Tools & Infrastructure Breakout Report Winter 2008

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RAMP requires RDF
Sharing, timing require only RDF

FSL Control
Probably two wires
Forward: WriteValid, Independent of actual

RDF vs RDL, Capital mistake to confuse the two
Need

RAMP & RDL are intended to be shared projects
Open source mentality,
Need more regular communication
Units (of course)

Teamwork
Design Sharing
Many independent, identical implementations
RDF & RDL are ways to share work
Units (of course)
Link/channel implementations
RAMP & RDL are intended to be shared projects

Linkage

RAMP & RDL are intended to be shared projects

FSL Link
Implementation varies based on Latency, Bitwidth, FIFO Depth

RDF Interfaces (1)
Write Side (Unit to Channel)
- Forward WriteValid, Independent
- Backward: Full, NotFull, Stall
Read Side (Channel to Unit)
- Forward Empty, NotEmpty, Stall
- Backward (ReadValid, ReadInvalid, Not)

Boolean
Need three value logic to separate read & write
- Probably two wires
- Independent of actual signaling convention

Attention with & bring general development about RDC

RDF Interfaces (2)

Teamwork
- Design Sharing
  - Many independent, identical implementations
  - RDF & RDL are ways to share work
  - Units (of course)
  - Link/channel implementations
  - RAMP & RDL are intended to be shared projects

- Need more regular communication
  - Desperate need for constructive criticism
  - Open source mentality, contribute your work back
  - Central CSV/SVN?
  - More often mini-retreats?
  - Phone Conferences?